_	Type	Hits	Search Text	DBs	
1	BRS	10	("5227677"   "5729152"   "60723 42"   "6266252"   "6356106"   "64 29678"   "6538951"   "6686763"   "6714465"   "6738844").PN.	USPAT	
2	BRS	6	"5729152"   "5926031"	US-PGPUB; USPAT; USOCR	
3	BRS	2	("6538951").URPN.	USPAT	
4	BRS	10	"6026456"   "6061754"	US-PGPUB; USPAT; USOCR	
5	BRS	5	"5663661"   "5790447"	US-PGPUB; USPAT; USOCR	
6	BRS	15	("6115278").URPN.	USPAT	
7	BRS	11.5		US-PGPUB; USPAT; USOCR	
8	BRS	26	("6067594").URPN.	USPAT	
9	BRS	4		US-PGPUB; USPAT; USOCR	
10	BRS	0	("6839786").URPN.	USPAT	
11	BRS	79	(memory adj module)) and terminat\$3 and clock and (chip adi select) and	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	

	Туре	Hits	Search Text	DBs
12	BRS	144	((memory adj chip) or (memory adj module)) and terminat\$3 and clock and impedance and ((main adj board) or (mother adj board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
13	BRS	109	S12 not S11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
14	BRS	1	("6594170").PN.	US-PGPUB; USPAT; USOCR
15	BRS	0	("6937494").URPN.	USPAT
16	BRS	128	((memory adj chip) or (memory adj module)) and terminat\$3 and clock and socket and ((main adj board) or (mother adj board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
17	BRS	73	S16 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
18	BRS	0	(memory or RAM or DRAM or SDRAM) and (dynamic adj terminat\$3) and clock and impedance and ((main adj board) or (mother adj board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
19	BRS		(memory or RAM or DRAM or SDRAM) and terminat\$3 and clock and impedance and ((main adj board) or (mother adj board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Type	Hits	Search Text	DBs	
20	BRS	229	S19 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	
21	BRS	229	S20 not S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM TDB	
22	BRS	24	(memory or RAM or DRAM or SDRAM) and (dynamic adj terminat\$3) and clock and impedance	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	
23	BRS	179	(memory or RAM or DRAM or SDRAM) and (active adj terminat\$3) and clock and impedance	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	
24	BRS	165	S23 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	

	Type	L	#	Hit	Search Text	DBs
1	BRS	L1		O	((memory adj module) and (memory adj chips) and terminated and (dynamic adj termination) and clock and (chip adj select)).clm.	US-PGPUB
2	BRS	L2			((memory adj module) and (memory adj chips) and (dynamic adj termination) and clock and (chip adj select)).clm.	US-PGPUB
3	BRS	L3			((memory adj module) and (memory adj chips) and (active adj terminator) and clock and (chip adj select)).clm.	US-PGPUB
4	BRS	L4			((memory adj module) and (memory adj chips) and (resistor adj means) and clock and (chip adj select)).clm.	US-PGPUB